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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Oelcer et al.

Serial No.: 10/045,810

Filed: January 11, 2002

For: BLOCK CODING FOR MULTILEVEL DATA COMMUNICATION

Docket No.: CH920000087US1

Group Art Unit:

Examiner:

Commissioner for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING UNDER 37 CFR 1.8(A)

I hereby certify that the attached correspondence comprising:

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Margaret   McCabe

(Signature of person mailing paper)

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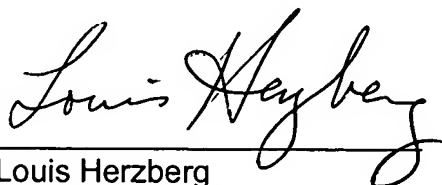
Examiner:

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D. C. 20231

Sir:

In order to assist in the examination of this application, and in accordance with 37 CFR 1.56, 1.97 and 1.98, copies of the documents listed on the attached form PTO-1449 are forwarded herewith.

By: _____
Louis Herzberg
Reg. No. 41,500
(914) 945-2885

IBM Corporation
Intellectual Property Law Dept.
P. O. Box 218
Yorktown Heights, New York 10598

FORM PTO-1449 (Modified)	ATTY. DOCKET NO.	SERIAL NO.
	CH920000087US1	10/045,810
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT: Oelcer et al.	
(Use several sheets if necessary)	FILING DATE: January 11, 2002	GROUP:

REFERENCE DESIGNATION   U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	"Reduced Complexity Iterative Decoding of Low-Density Parity Check Codes Based on Belief Propagation", Marc P.C. Fossorier, Miodrag Mihaljevic, Hideki Imai.
	AN	"Irregular Repeat-Accumulate Codes": Hui Jin, Aamod Khandekar, and robert McEliece; Dept. of Electrical Eng., California Institute of Technology
	AO	"Reed-Muller Coding for Partial Response Channels", Sedat Olcer and Gottfried Ungerboeck; IBM research Div., Zurich Research Lab. Switzerland
	AP	"Bandwidth Efficient Low Density Parity Check Coding using Multi Level Coding and Iterative Multi Stage Decoding", K.R. Narayanan and J. Li; Texas A&M University, College Sta. Texas.
	AQ	Catena Networks, Inc., "A Turbo TCM scheme with Low Decoding Complexity" Bangalore, India, Study group.
	AR	Mitsubishi Elec. Corp.: "Proposal of Dicision making process for Turbo Coding & report of performance evaluation of proposed TTCM(PCC)with R-S code and without R-S code.
	AS	VOCAL Technologies Ltd.; "Results of requirements requested in the Coding Ad hoc report(BA-108R1)for the proposed Turbo Codes for ADSL modems by VoCAL Technologies Ltd. in BA-020R1.
	AT	Globespan: "Block Product Turbo Codes for G.dmt.bis and G.lite.bis.
	AU	Aware, Inc."Low Density Parity Check Coded Modulation for ADSL.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.